

Γ

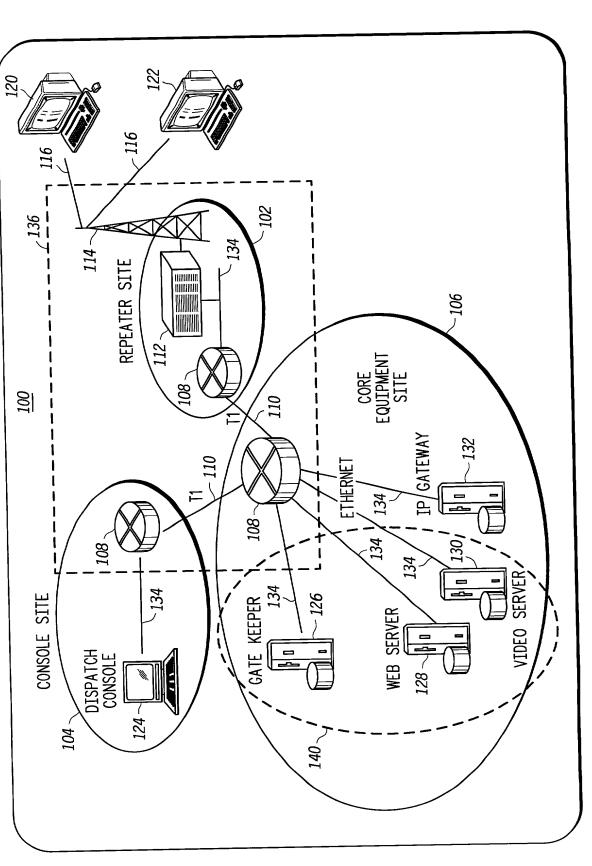


FIG.1

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SLOT HEADER 205

> DATA BLOCK <u>210</u>

DATA BLOCK <u>210</u>

200

MAC HEADER *305* LINK LAYER HEADER 310 LINK LAYER HEADER 310 CRC <u>320</u>

205

FIG.2

FIG.3

SLOT TYPE <u>405</u>	RES <u>425</u>
NEXT SLOT COMMUNIC UNIT ID NUMBE	
MAC DESTINATI ID NUMBER	ON <u>415</u>
MAC SOURCE ID NUMBER	<u>420</u>

305

RES SLOT TYPE <u>525</u> *505* MAC DESTINATION ID NUMBER <u>515</u> MAC SOURCE ID NUMBER *520*

<u>305</u>

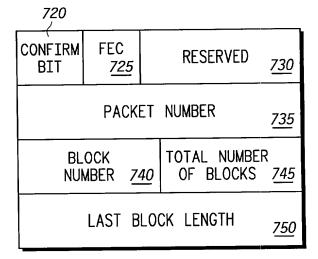
FIG.4

FIG.5

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ACKNOWLEDGEMENT 605 ID NUMBER **ACKNOWLEDGEMENT** 610 PACKET NUMBER 615 **ACKNOWLEDGEMENT ACK RESERVED** BLOCK NUMBER BIT 630 625 **FEC** CONFIRM **RESERVED** 630 BIT 622 620 PACKET NUMBER 635

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310

FIG.7

<u>310</u>

TOTAL NUMBER

OF BLOCKS

645

650

FIG.6

LAST BLOCK LENGTH

640

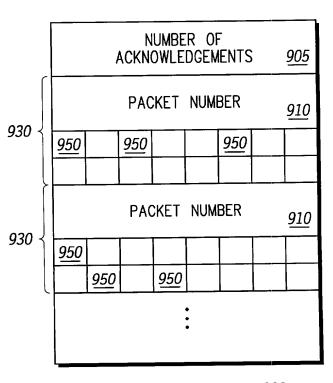
BLOCK

NUMBER

TRANSMITTED
DATA
805
CRC 810

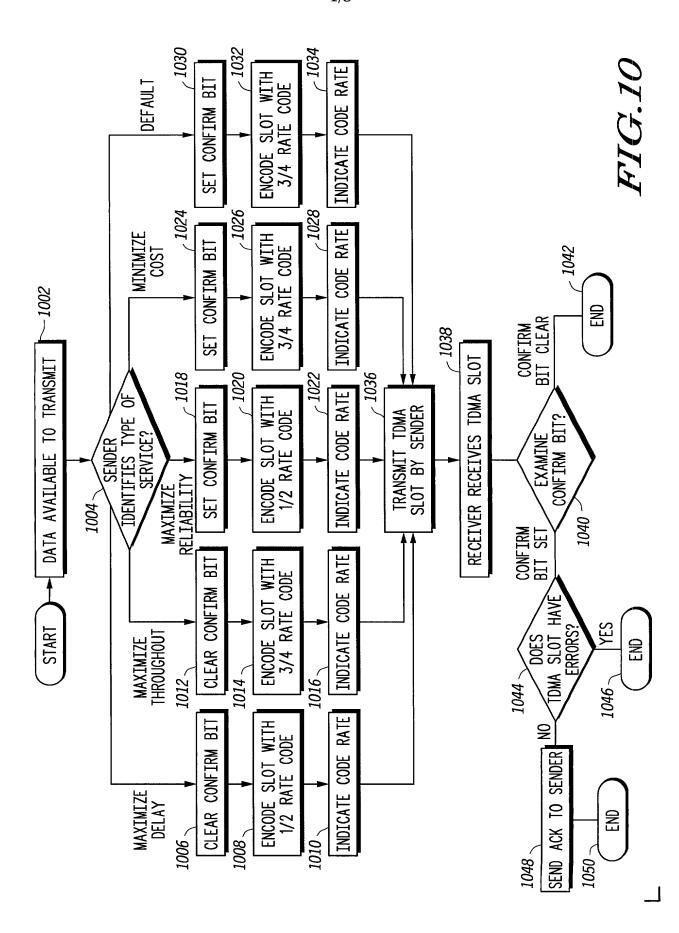
<u>210</u>

FIG.8



900

FIG.9



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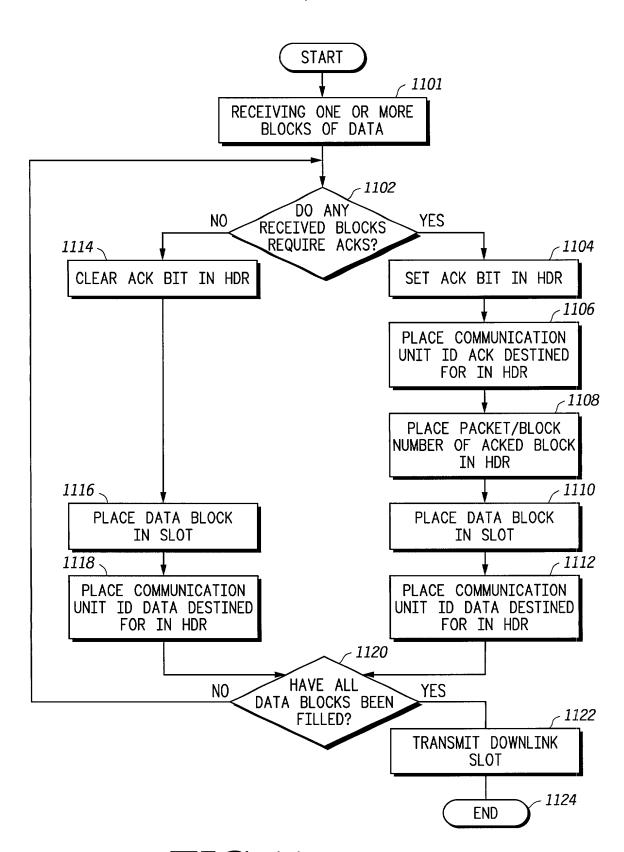


FIG.11

1

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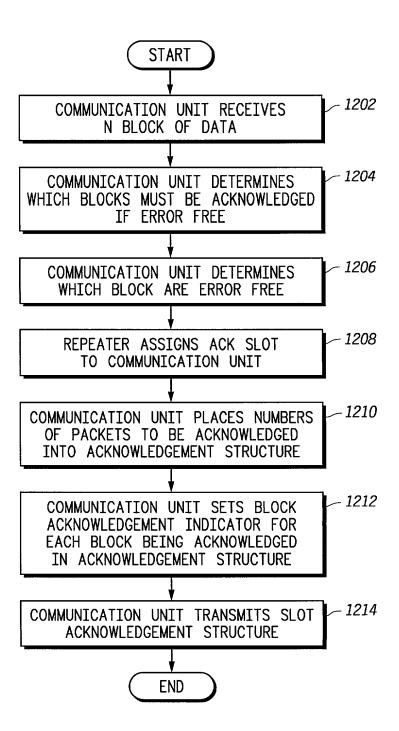


FIG.12

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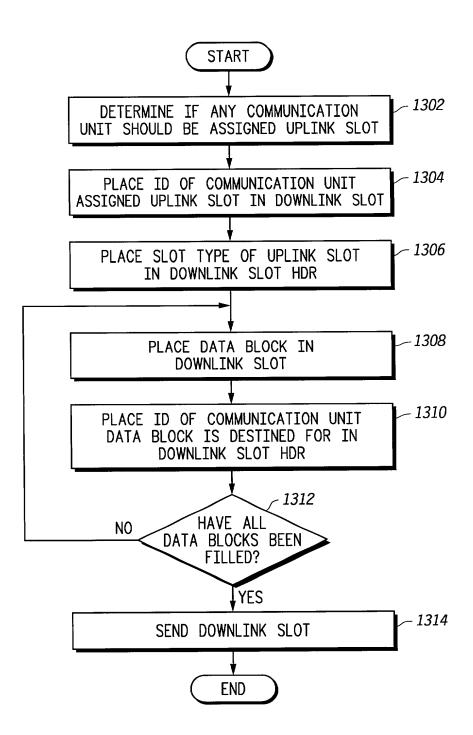


FIG.13

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MAC HEADER	<u>1405</u>
CRC	<u>1430</u>
LINK LAYER HEADER	<u>1410</u>
DATA BLOCK	1420
LINK LAYER HEADER	<u>1412</u>
DATA BLOCK	<u>1422</u>

1400

FIG.14

